

Practitioner's Docket No. NVIDP033A/P000873

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: James M. Van Dyke et al.

Application No.: 10/657,957

Group No.: 2186

Filed: 09/08/2003

Examiner: Woo H. Choi

For: SYSTEM AND METHOD FOR A HIGH BANDWIDTH-LOW LATENCY MEMORY CONTROLLER

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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A DOUBLE PATENTING REJECTION (37 C.F.R. § 1.321(c))

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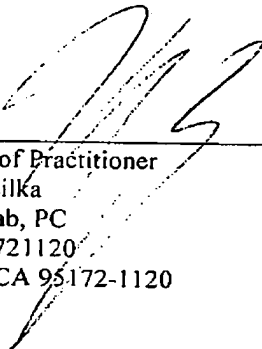
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\_\_\_\_\_  
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Kevin J. Zilka  
Zilka-Kotab, PC  
P.O. Box 721120  
San Jose, CA 95172-1120  
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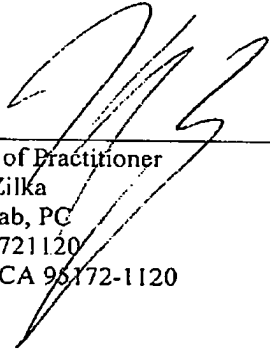
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Zilka-Kotab, PC  
P.O. Box 721120  
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